

## AMENDMENTS TO THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended) An apparatus comprising:  
a first audio input/output (I/O) connector provided for coupling to a first audio I/O device;  
a second audio I/O connector provided for coupling to a second audio I/O device;  
the first and second connectors being coupled to an audio controller;  
a transistor coupled to the first and second connectors and to ground, the transistor connected to pull the first device coupled to the first I/O connector to a zero voltage level when the second device is coupled to the second I/O connector;  
~~a primary audio input disable signal coupling; and~~  
~~a disabling device and a circuit element, the circuit element coupling a filter circuit with an inverting amplifier providing a DC blocking cap,~~  
~~whereby the primary audio input disable signal coupling, the disable device and the circuit element are coupled between the first and second I/O connectors.~~  
a PCI bus connecting a PCI card slot to a card/bus controller; and  
the audio controller connected to the PCI bus.
2. (Currently Amended) The apparatus of Claim 1, further comprising an integrating amplifier I/O controller hub connected to the PCI bus.
3. (Currently Amended) The apparatus of Claim 2, further comprising ~~an~~ audio I/O coupling a super I/O controller connected to the I/O controller hub.

4. (Cancelled).
5. (Cancelled).
6. (Original) The apparatus of Claim 1, wherein the first audio ~~input/output~~ I/O connector comprises a jack.
7. (Original) The apparatus of Claim 1, wherein the second audio ~~input/output~~ I/O connector comprises a jack.
8. (Currently Amended) A computer system, comprising:
  - a processor;
  - a memory coupled to the processor;
  - an audio controller coupled to the processor;
  - a first audio I/O connector coupled to the audio controller and provided for coupling to a first audio I/O device;
  - a second audio I/O connector coupled to the audio controller and provided for coupling to a second audio I/O device;
  - a transistor coupled to the first and second connectors and to ground, the transistor connected to pull the first device coupled to the first I/O connector to a zero voltage level when the second device is coupled to the second I/O connector;
  - ~~a primary audio input disable signal coupling; and~~
  - ~~a disabling device and a circuit element, the circuit element coupling a filter circuit with an inverting amplifier providing a DC blocking cap,~~
  - ~~whereby the primary audio input disable signal coupling, the disable device and the circuit element are coupled between the first and second I/O~~

~~connectors.~~

connecting a PCI bus to a PCI card slot and to a card/bus controller; and  
connecting the audio controller to the PCI bus.

9. (Currently Amended) The computer system of Claim 8, further  
comprising ~~an integrating amplifier~~ I/O controller hub connected to the PCI bus.

10. (Currently Amended) The computer system of Claim 9, further  
comprising ~~an audio I/O coupling~~ a super I/O controller connected to the I/O controller  
hub.

11. (Cancelled).

12. (Cancelled).

13. (Original) The computer system of Claim 8, wherein the first audio  
~~input/output~~ I/O connector is a jack.

14. (Original) The computer system of Claim 13, wherein the second audio  
~~input/output~~ I/O connector comprises a jack.

15. (Original) The computer system of Claim ~~8~~ 10, wherein the first and  
second audio ~~input/output connector comprises~~ I/O connectors each comprise a jack.

16. (Cancelled).